

Electronic Acknowledgement Receipt

EFS ID:	9888326
Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor/Applicant Name:	Guido Gabriele Albasini
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	2110-111-03_Issue_Fee_Transmittal_20110415.pdf	99307 2eb76a71d51b88f7c9ad87938a6e119263c02f1	no	1
Warnings:					
Information:					
2	Miscellaneous Incoming Letter	2110-111-03_Comments_on_Reasons_for_Allowance_20110415.pdf	116109 e71a2920ba9912a04b3ak4d1ee628c6ca05286a	no	2
Warnings:					
Information:					
3		2110-111-03_312_amendment_20110415.pdf	67771 ad13c75d689064c3c5f33af1a7c01fb7b134f	yes	9
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment after Notice of Allowance (Rule 312)		1	1	
	Claims		2	8	
	Applicant Arguments/Remarks Made in an Amendment		9	9	
Warnings:					
Information:					
4	Fee Worksheet (PTO-875)	fee-info.pdf	33632 a88a6e4c7734fb514c37292c58b6f11708446ca6	no	2
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Total Files Size (in bytes):			316819		

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